



Digital Electronics

Final Examination

Part A

Multiple Choice

ANSWER KEY

Spring 2009
PRACTICE EXAMS

For Teacher Use ONLY

Part A – Multiple Choice Questions

Question	Answer	DE Assessment Concepts
1	B	1 – Fundamentals
2	D	1 – Fundamentals
3	C	1 – Fundamentals
4	D	1 – Fundamentals
5	B	1 – Fundamentals
6	B	1 – Fundamentals
7	A	2 – Number Systems
8	B	2 – Number Systems
9	C	3 – Gates
10	B	3 – Gates
11	C	4 – Boolean Algebra
12	A	4 – Boolean Algebra
13	D	4 – Boolean Algebra
14	B	4 – Boolean Algebra
15	A	4 – Boolean Algebra
16	C	4 – Boolean Algebra
17	D	5 – Combinational Logic Circuit Design
18	D	5 – Combinational Logic Circuit Design
19	C	5 – Combinational Logic Circuit Design
20	A	5 – Combinational Logic Circuit Design
21	C	5 – Combinational Logic Circuit Design
22	A	5 – Combinational Logic Circuit Design
23	C	5 – Combinational Logic Circuit Design
24	A	5 – Combinational Logic Circuit Design
25	D	6 – Adding
26	A	6 – Adding
27	B	7 – Flip Flops
28	B	7 – Flip Flops
29	C	7 – Flip Flops
30	B	7 – Flip Flops
31	B	7 – Flip Flops
32	D	7 – Flip Flops
33	B	8 – Shift Registers & Counters
34	D	8 – Shift Registers & Counters
35	C	8 – Shift Registers & Counters
36	D	8 – Shift Registers & Counters
37	A	8 – Shift Registers & Counters
38	C	8 – Shift Registers & Counters
39	D	9 – Families & Specifications
40	C	9 – Families & Specifications

Answer Breakdown: A-8; B-11; C-11; D-10

Part A Scoring Conversion Chart

Raw Score	Conversion	Raw Score	Conversion	Raw Score	Conversion	Raw Score	Conversion
40	50	30	38	20	25	10	13
39	49	29	36	19	24	9	11
38	48	28	35	18	23	8	10
37	46	27	34	17	21	7	9
36	45	26	33	16	20	6	8
35	44	25	31	15	19	5	6
34	43	24	30	14	18	4	5
33	41	23	29	13	16	3	4
32	40	22	28	12	15	2	3
31	39	21	26	11	14	1	1